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| **4** | **Intel 80386DX Processor** | | **7** |
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|  | **4.1** | Architecture of 80386 microprocessor |  |
| **4.2** | 80386 registers–General-purpose Registers, EFLAGS, and Control  registers. |
|  | **4.3** | Real mode, Protected mode, virtual 8086 mode |  |
|  | **4.4** | 80386 memory management in Protected Mode – Descriptors and selectors, descriptor tables, the memory paging mechanism |  |

**Module 4**

**Intel 80386DX Processor**

4.1Architecture of 80386 microprocessor

4.280386 registers–General purpose Registers, EFLAGS and Control registers

4.3Real mode, Protected mode, virtual 8086 mode

4.480386 memory management in Protected Mode – Descriptors and selectors, descriptor tables, the memory paging mechanism